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U.S. PATENT APPLICATION

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07/743,383	08/21/91	156	1304

APPLICANT

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CONTINUING DATA********
VERIFIED

FOREIGN/PCT APPLICATIONS********

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TITLE

METHOD FOR PRODUCING SEMICONDUCTOR INTEGRATED CIRCUITS AND APPARATUS USED IN SUCH METHOD

This is to certify that annexed hereto is a true copy from the records of the United States Patent and Trademark Office of the application as filed which is identified above.

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Date Certifying Officer